

## **Product Specification**

### **8.5 Gb/s Short-Wavelength SFP+ Transceiver**

#### **FTLF8528P2BNV**

#### **PRODUCT FEATURES**

- Up to 8.5 Gb/s bi-directional data links
- Hot-pluggable SFP+ footprint
- Built-in digital diagnostic functions
- 850nm Oxide VCSEL laser transmitter
- Duplex LC connector
- RoHS compliant and Lead Free
- 50m on 50/125µm MMF
- 150m on high-bandwidth 50/125µm (OM3) MMF
- Metal enclosure, for lower EMI
- Single 3.3V power supply
- Extended operating temperature range: -5°C to 85°C



#### **APPLICATIONS**

- Tri-Rate 2.125/4.25/8.5 Gb/s Fibre Channel

Finisar's FTLF8528P2BNV SFP+ transceivers are designed for use in Fibre Channel links up to 8.5 Gb/s data rate over multimode fiber. They are compliant with FC-P1-4 Rev. 7.00<sup>1</sup> and SFF-8472 Rev 10.1<sup>3</sup>, and compatible with SFF-8432<sup>2</sup> and applicable portions of SFF-8431 Rev. 1.3<sup>4</sup>. The product is RoHS compliant and lead-free per Directive 2002/95/EC<sup>5</sup> and Finisar Application Note AN-2038.

#### **PRODUCT SELECTION**

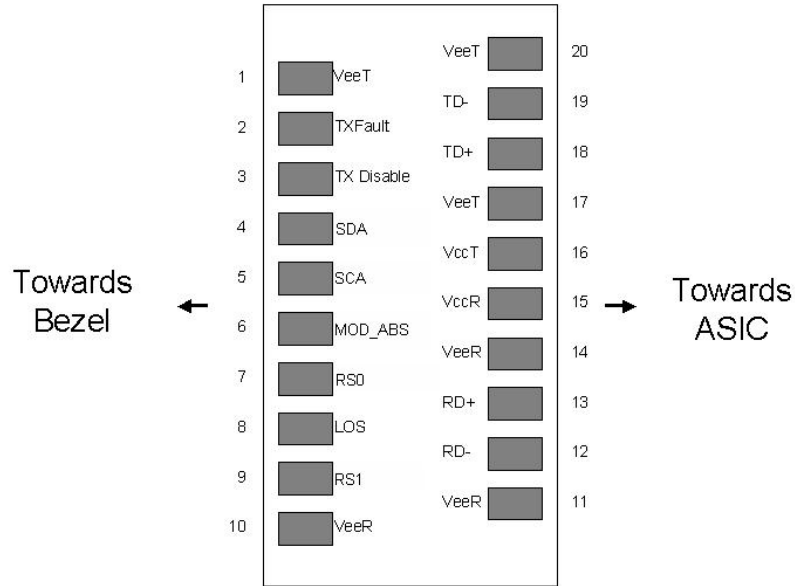
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|----------------------|
| <b>FTLF8528P2BNV</b> |
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**I. Pin Descriptions**

| Pin | Symbol             | Name/Description                                                                                                                    | Ref. |
|-----|--------------------|-------------------------------------------------------------------------------------------------------------------------------------|------|
| 1   | V <sub>EET</sub>   | Transmitter Ground (Common with Receiver Ground)                                                                                    | 1    |
| 2   | T <sub>FAULT</sub> | Transmitter Fault.                                                                                                                  | 2    |
| 3   | T <sub>DIS</sub>   | Transmitter Disable. Laser output disabled on high or open.                                                                         | 3    |
| 4   | SDA                | 2-wire Serial Interface Data Line (MOD-DEF2)                                                                                        | 4    |
| 5   | SCA                | 2-wire Serial Interface Clock (MOD-DEF1)                                                                                            | 4    |
| 6   | MOD_ABS            | Module Absent, connected to V <sub>EET</sub> or V <sub>EER</sub>                                                                    | 4    |
| 7   | RS0                | Rx Rate Select:<br>Open or Low = 2.125 or 4.25 Gb/s Fibre Channel (Low Bandwidth)<br>High = 8.5 Gb/s Fibre Channel (High Bandwidth) | 5    |
| 8   | LOS                | Loss of Signal indication. Logic 0 indicates normal operation.                                                                      | 6    |
| 9   | RS1                | Tx Rate Select:<br>Open or Low = 2.125 or 4.25 Gb/s Fibre Channel (Low Bandwidth)<br>High = 8.5 Gb/s Fibre Channel (High Bandwidth) | 5    |
| 10  | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                    | 1    |
| 11  | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                    | 1    |
| 12  | RD-                | Receiver Inverted DATA out. AC Coupled                                                                                              |      |
| 13  | RD+                | Receiver Non-inverted DATA out. AC Coupled                                                                                          |      |
| 14  | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                    | 1    |
| 15  | V <sub>CCR</sub>   | Receiver Power Supply                                                                                                               |      |
| 16  | V <sub>CCT</sub>   | Transmitter Power Supply                                                                                                            |      |
| 17  | V <sub>EET</sub>   | Transmitter Ground (Common with Receiver Ground)                                                                                    | 1    |
| 18  | TD+                | Transmitter Non-Inverted DATA in. AC Coupled.                                                                                       |      |
| 19  | TD-                | Transmitter Inverted DATA in. AC Coupled.                                                                                           |      |
| 20  | V <sub>EET</sub>   | Transmitter Ground (Common with Receiver Ground)                                                                                    | 1    |

**Notes:**

1. Circuit ground is internally isolated from chassis ground.
2. T<sub>FAULT</sub> is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to V<sub>CC</sub> + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on T<sub>DIS</sub> >2.0V or open, enabled on T<sub>DIS</sub> <0.8V.
4. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. MOD\_ABS pulls line low to indicate module is plugged in.
5. Rate select can also be set through the 2-wire bus in accordance with SFF-8472 v. 10.1<sup>3</sup>. Rx Rate Select is set at Bit 3, Byte 110, Address A2h. Tx Rate Select is set at Bit 3, Byte 118, Address A2h. Note: writing a “1” selects maximum bandwidth operation. Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus.
6. LOS is open collector output. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

**Diagram of Host Board Connector Block Pin Numbers and Names****II. Absolute Maximum Ratings**

| Parameter                          | Symbol         | Min  | Typ | Max | Unit | Ref. |
|------------------------------------|----------------|------|-----|-----|------|------|
| Maximum Supply Voltage             | Vcc            | -0.5 |     | 4.0 | V    |      |
| Storage Temperature                | T <sub>S</sub> | -40  |     | 85  | °C   |      |
| Case Operating Temperature         | T <sub>A</sub> | -5   |     | 85  | °C   |      |
| Relative Humidity (Non-condensing) | RH             | 0    |     | 85  | %    |      |

**III. Electrical Characteristics (T<sub>A</sub> = -5 to 85 °C, V<sub>CC</sub> = 3.0 to 3.6 Volts)**

| Parameter                                      | Symbol                          | Min             | Typ | Max                   | Unit | Ref. |
|------------------------------------------------|---------------------------------|-----------------|-----|-----------------------|------|------|
| Supply Voltage                                 | V <sub>CC</sub>                 | 3.0             |     | 3.6                   | V    |      |
| Supply Current                                 | I <sub>CC</sub>                 |                 |     | 240                   | mA   |      |
| <b>Transmitter</b>                             |                                 |                 |     |                       |      |      |
| Input differential impedance                   | R <sub>in</sub>                 |                 | 100 |                       | Ω    | 1    |
| Single ended data input swing                  | V <sub>in,pp</sub>              | 90              |     | 800                   | mV   |      |
| Transmit Disable Voltage                       | V <sub>D</sub>                  | 2               |     | V <sub>CC</sub>       | V    | 2    |
| Transmit Enable Voltage                        | V <sub>EN</sub>                 | V <sub>EE</sub> |     | V <sub>EE</sub> + 0.8 | V    |      |
| <b>Receiver</b>                                |                                 |                 |     |                       |      |      |
| Single ended data output swing                 | V <sub>out,pp</sub>             | 170             |     | 400                   | mV   | 3    |
| Data Output Rise/Fall Time @ 2.125, 4.25 Gb/s  | t <sub>r</sub> / t <sub>f</sub> |                 |     | 120                   | ps   | 4    |
| Data Output Rise/Fall Time @ 8.5 Gb/s          | t <sub>r</sub> / t <sub>f</sub> |                 |     | 60                    | ps   | 4    |
| LOS Fault                                      | V <sub>LOS fault</sub>          | 2               |     | V <sub>CCHOST</sub>   | V    | 5    |
| LOS Normal                                     | V <sub>LOS norm</sub>           | V <sub>EE</sub> |     | V <sub>EE</sub> +0.8  | V    | 5    |
| Power Supply Rejection                         | PSR                             | 100             |     |                       | mVpp | 6    |
| Deterministic Jitter Contribution @ 2.125 Gb/s | RX Δ DJ                         |                 |     | 47.1                  | ps   |      |
| Total Jitter Contribution @ 2.125 Gb/s         | RX Δ TJ                         |                 |     | 123.5                 | ps   | 7    |
| Deterministic Jitter Contribution @ 4.25 Gb/s  | RX Δ DJ                         |                 |     | 23.5                  | ps   |      |
| Total Jitter Contribution @ 4.25 Gb/s          | RX Δ TJ                         |                 |     | 61.8                  | ps   | 8    |
| Deterministic Jitter @ 8.5 Gb/s                | RX DJ                           |                 |     | 49.4                  | ps   | 8    |
| Pulse Width Shrinkage @ 8.5 Gb/s               | RX<br>DDPWS                     |                 |     | 42.4                  | ps   | 8    |
| Total Jitter @ 8.5 Gb/s                        | RX TJ                           |                 |     | 83.5                  | ps   | 8    |

**Notes:**

1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
2. Or open circuit.
3. Into 100 ohms differential termination.
4. Unfiltered, 20 – 80 %
5. LOS is an open collector output. Should be pulled up with 4.7k – 10kohms on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.
6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.
7. If measured with TJ-free data input signal. In actual application, output TJ will be given by:

$$TJ_{OUT} = DJ_{IN} + \Delta DJ + \sqrt{(TJ_{IN} - DJ_{IN})^2 + (\Delta TJ - \Delta DJ)^2}$$

8. As defined in FC-PI-4<sup>1</sup>, Rev 7.0, Table 13, 800-Mx-SN-y.

**IV. Optical Characteristics (T<sub>A</sub> = -5 to 85 °C, V<sub>CC</sub> = 3.0 to 3.6 Volts)**

| Parameter                                              | Symbol                          | Min | Typ | Max   | Unit  | Ref. |
|--------------------------------------------------------|---------------------------------|-----|-----|-------|-------|------|
| <b>Transmitter</b>                                     |                                 |     |     |       |       |      |
| Output Opt. Pwr: 50 or 62.5 MMF                        | P <sub>OUT</sub>                | -9  |     | -2    | dBm   | 1    |
| Optical Wavelength                                     | λ                               | 830 |     | 860   | nm    |      |
| Spectral Width                                         | σ                               |     |     | 0.65  | nm    |      |
| Optical Modulation Amplitude @ 2.125 Gb/s              | OMA                             | 196 |     |       | μW    | 2    |
| Optical Modulation Amplitude @ 4.25 Gb/s               | OMA                             | 247 |     |       | μW    | 2    |
| Optical Modulation Amplitude @ 8.5 Gb/s                | OMA                             | 302 |     |       | μW    | 2    |
| Optical Rise/Fall Time @ 2.125, 4.25 Gb/s              | t <sub>r</sub> / t <sub>f</sub> |     |     | 90    | ps    | 3    |
| Transmitter Waveform and Dispersion Penalty @ 8.5 Gb/s | TWDP                            |     |     | 4.2   | dB    | 4    |
| Relative Intensity Noise                               | RIN                             |     |     | -128  | dB/Hz |      |
| Deterministic Jitter Contribution @ 2.125 Gb/s         | TX Δ DJ                         |     |     | 56.5  | ps    |      |
| Total Jitter Contribution @ 2.125 Gb/s                 | TX Δ TJ                         |     |     | 119.6 | ps    | 5    |
| Deterministic Jitter Contribution @ 4.25 Gb/s          | TX Δ DJ                         |     |     | 28.2  | ps    |      |
| Total Jitter Contribution @ 4.25 Gb/s                  | TX Δ TJ                         |     |     | 59.8  | ps    | 5    |
| <b>Receiver</b>                                        |                                 |     |     |       |       |      |
| Receiver OMA Sensitivity = 2.125 Gb/s                  | RxSENS                          |     |     | 49    | μW    |      |
| Receiver OMA Sensitivity = 4.25 Gb/s                   | RxSENS                          |     |     | 61    | μW    |      |
| Receiver OMA Sensitivity = 8.5 Gb/s                    | RxSENS                          |     |     | 76    | μW    |      |
| Average Receiver Power                                 | Rx <sub>MAX</sub>               | 0   |     |       | dBm   |      |
| Optical Center Wavelength                              | λ <sub>C</sub>                  | 770 |     | 860   | nm    |      |
| Optical Return Loss                                    |                                 | 12  |     |       | dB    |      |
| LOS De-Assert                                          | LOS <sub>D</sub>                |     |     | -18   | dBm   |      |
| LOS Assert                                             | LOS <sub>A</sub>                | -30 |     |       | dBm   |      |
| LOS Hysteresis                                         |                                 | 0.5 |     |       | dB    |      |

Notes:

1. Class 1 Laser Safety per FDA/CDRH, and EN (IEC) 60825 laser safety standards.
2. Equivalent extinction ratio specification for Fibre Channel. Allows smaller ER at higher average power.
3. Unfiltered, 20-80%. Complies with FC 1x and 2x eye mask when filtered.
4. TWDP is calculated with a 1.0 equalizer and a 6,860 MHz Gaussian filter for the fiber simulation. Jitter values at γ<sub>T</sub> and γ<sub>R</sub> are controlled by TWDP and stress receiver sensitivity.
5. If measured with TJ-free data input signal. In actual application, output TJ will be given by:

$$TJ_{OUT} = DJ_{IN} + \Delta DJ + \sqrt{(TJ_{IN} - DJ_{IN})^2 + (\Delta TJ - \Delta DJ)^2}$$

**V. General Specifications**

| Parameter                                         | Symbol | Min | Typ                    | Max               | Units  | Ref.        |
|---------------------------------------------------|--------|-----|------------------------|-------------------|--------|-------------|
| Data Rate                                         | BR     |     | 2.125,<br>4.25,<br>8.5 |                   | Gb/sec | 1           |
| Bit Error Rate                                    | BER    |     |                        | $10^{-12}$        |        | 2           |
| Fiber Length on 50/125µm MMF                      | L      |     |                        | 300<br>150<br>50  | m      | 3<br>4<br>5 |
| Fiber Length on 50/125µm high-bandwidth (OM3) MMF | L      |     |                        | 500<br>380<br>150 | m      | 3<br>4<br>5 |

Notes:

1. 2x and 4x Fibre Channel compatible, per FC-P1-4<sup>1</sup>.
2. PRBS 2<sup>7</sup>-1.
3. At 2.125 Gb/s Fibre Channel data rate.
4. At 4.25 Gb/s Fibre Channel data rate.
5. At 8.5 Gb/s Fibre Channel data rate.

**VI. Environmental Specifications**

Finisar 850nm SFP+ transceivers have an extended operating temperature range from -5°C to +85°C case temperature.

| Parameter                  | Symbol           | Min | Typ | Max | Units | Ref. |
|----------------------------|------------------|-----|-----|-----|-------|------|
| Case Operating Temperature | T <sub>op</sub>  | -5  |     | 85  | °C    |      |
| Storage Temperature        | T <sub>sto</sub> | -40 |     | 85  | °C    |      |

**VII. Regulatory Compliance**

Finisar transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified by TÜV and CSA to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available at Finisar Corporation upon request.

## VIII. Digital Diagnostic Functions

Finisar FTLF8528P2BNV SFP+ transceivers support the 2-wire serial communication protocol as defined in the SFP MSA<sup>6</sup>. It is very closely related to the E<sup>2</sup>PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar SFP transceivers provide an enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E<sup>2</sup>PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN-2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E<sup>2</sup>PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more information, please see the SFP MSA documentation<sup>3,6</sup> and Finisar Application Note AN-2030.

Please note that evaluation board FDB-1027 is available with Finisar ModDEMO software that allows simple to use communication over the 2-wire serial interface.

**IX. Digital Diagnostic Specifications**

FTLF8528P2BNV transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

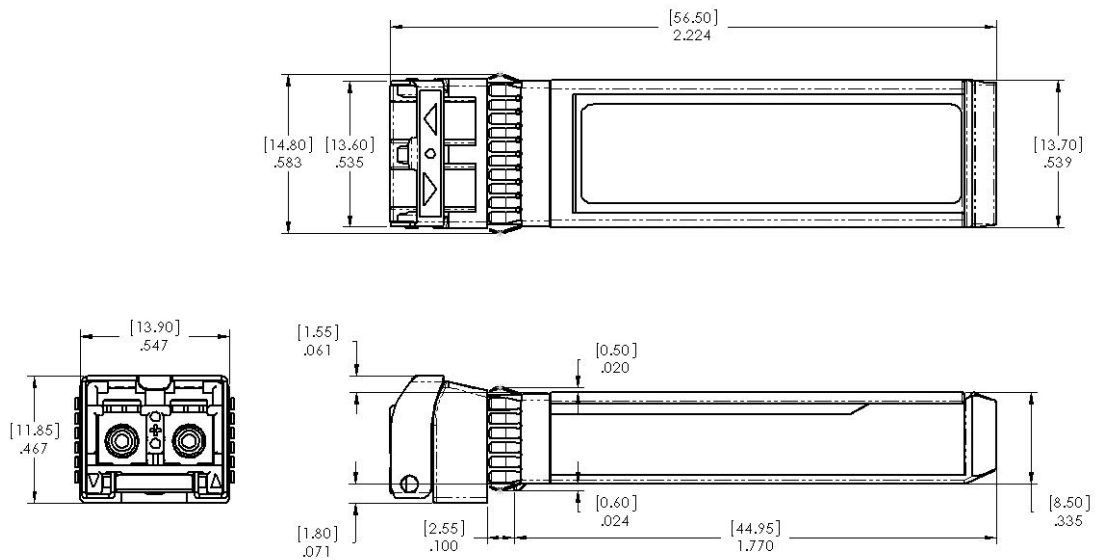
| Parameter                                      | Symbol                    | Min | Typ | Max | Units | Ref. |
|------------------------------------------------|---------------------------|-----|-----|-----|-------|------|
| <b>Accuracy</b>                                |                           |     |     |     |       |      |
| Internally measured transceiver temperature    | DD <sub>Temperature</sub> |     |     | 3   | °C    |      |
| Internally measured transceiver supply voltage | DD <sub>Voltage</sub>     |     |     | 100 | mV    |      |
| Measured TX bias current                       | DD <sub>Bias</sub>        |     |     | 10  | %     | 1    |
| Measured TX output power                       | DD <sub>Tx-Power</sub>    |     |     | 2   | dB    |      |
| Measured RX received average optical power     | DD <sub>Rx-Power</sub>    |     |     | 2   | dB    |      |
| <b>Dynamic Range for Rated Accuracy</b>        |                           |     |     |     |       |      |
| Internally measured transceiver temperature    | DD <sub>Temperature</sub> | -5  |     | 85  | °C    |      |
| Internally measured transceiver supply voltage | DD <sub>Voltage</sub>     | 3.0 |     | 3.6 | V     |      |
| Measured TX bias current                       | DD <sub>Bias</sub>        | 0   |     | 20  | mA    |      |
| Measured TX output power                       | DD <sub>Tx-Power</sub>    | -9  |     | -2  | dBm   |      |
| Measured RX received average optical power     | DD <sub>Rx-Power</sub>    | -18 |     | 0   | dBm   |      |
| <b>Max Reporting Range</b>                     |                           |     |     |     |       |      |
| Internally measured transceiver temperature    | DD <sub>Temperature</sub> | -40 |     | 125 | °C    |      |
| Internally measured transceiver supply voltage | DD <sub>Voltage</sub>     | 2.8 |     | 4.0 | V     |      |
| Measured TX bias current                       | DD <sub>Bias</sub>        | 0   |     | 20  | mA    |      |
| Measured TX output power                       | DD <sub>Tx-Power</sub>    | -10 |     | -2  | dBm   |      |
| Measured RX received average optical power     | DD <sub>Rx-Power</sub>    | -20 |     | 0   | dBm   |      |

**Notes:**

1. Accuracy of Measured Tx Bias Current is 10% of the actual Bias Current from the laser driver to the laser.

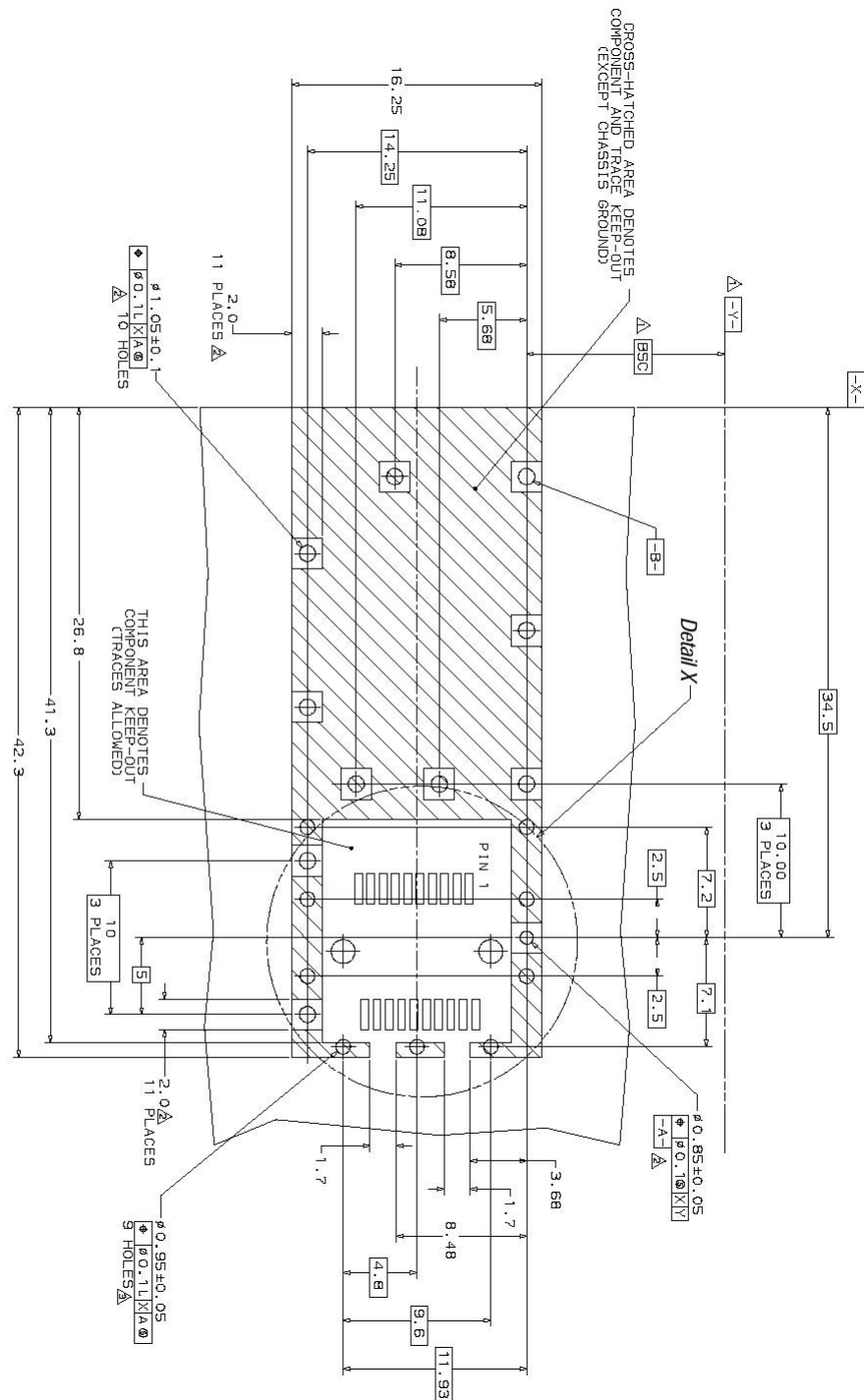
## X. Mechanical Specifications

Finisar's FTLF8528P2BNV SFP+ transceivers are compatible with the SFF-8432<sup>2</sup> specification for improved pluggable form factor.



### FTLF8528P2BNV

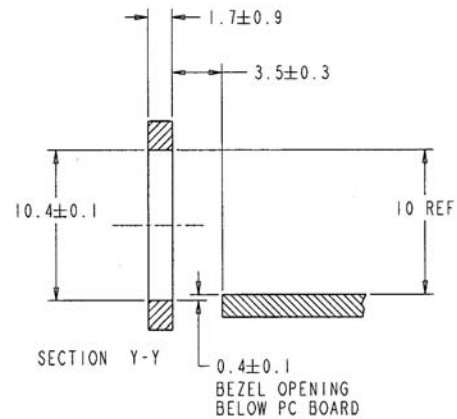
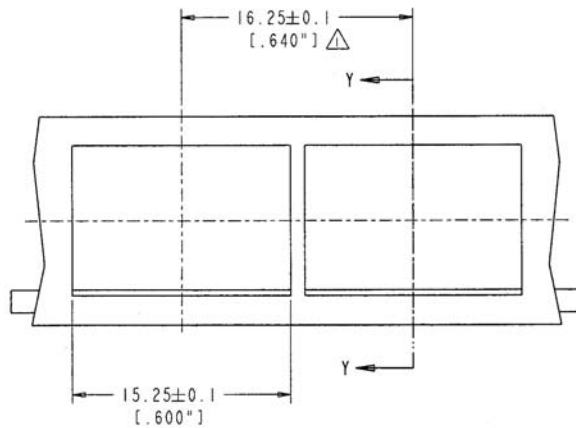
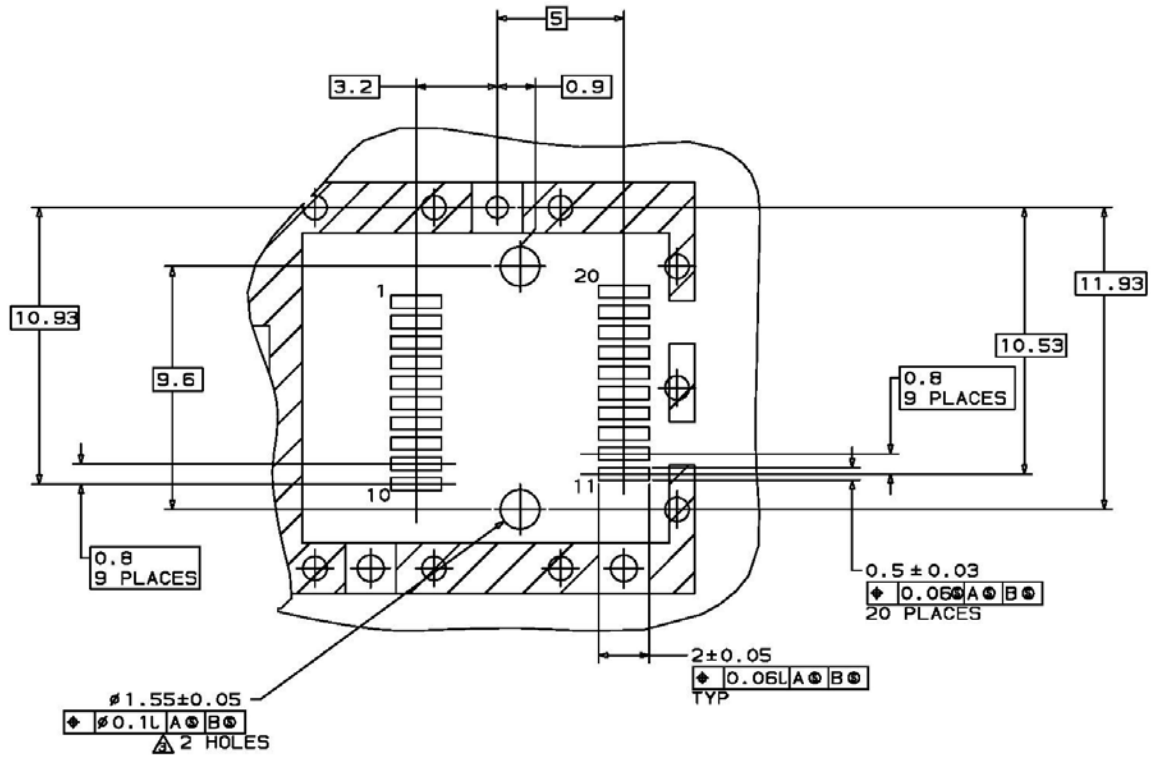
## XI. PCB Layout and Bezel Recommendations



### Datum and Basic Dimension Established by Customer

## ⚠️ Rads and Vias are Chassis Ground, 11 Places

### ⚠ Through Holes are Unplated



## NOTES:

△ MINIMUM PITCH ILLUSTRATED, ENGLISH DIMENSIONS ARE FOR REFERENCE ONLY

2. NOT RECOMMENDED FOR PCI EXPANSION CARD APPLICATIONS

**XII. References**

1. “Fibre Channel Physical Interface-4 Specification (FC-PI-4 Rev. 7.00)”. American National Standard for Information Systems, September 20, 2007.
2. “Improved Pluggable Formfactor”, SFF Document Number SFF-8432, Revision 5.0, July 16, 2007.
3. “Digital Monitoring Interface for Optical Transceivers”, SFF Document Number SFF-8472, Revision 10.2, June 1, 2007.
4. “Enhanced Specification for 8.5 and 10 Gigabit Small Form Factor Pluggable Module ‘SFP+ ’”, SFF Document Number SFF-8431, Revision 2.1, August 30, 2007.
5. Directive 2002/95/EC of the European Council Parliament and of the Council, “on the restriction of the use of certain hazardous substances in electrical and electronic equipment.” January 27, 2003.
6. Small Form Factor Pluggable (SFP) Transceiver Multi-source Agreement (MSA), September 14, 2000.

**XIII. For More Information**

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